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APPLICATION NO.	FILING DATE	FILING DATE		FIRST NAMED INVEN		OR A		ORNEY DOCKET NO.	CONFIRMATION NO.	
10/770,711	10/770,711 02/02/2004							H0897	2278	
TITLE OF INVENTION SEMICONDUCTOR WA	: PREDICTING DEFEC AFER LOT DISPOSITION	T FUT	URE EFFECTS II	N INTEGRATED CIF	CUI	T TECHNOL	OGY DEV			
APPLN. TYPE	SMALL ENTITY	ENTITY ISSUE FE		PUBLICATION FEE DI		PREV. PAID ISSUE FEE		TOTAL FEE(S) DUE	DATE DUE	
nonprovisional	NO	NO		\$0		\$0		\$1400	06/14/2007	
EXAMINER			ART UNIT	CLASS-SUBCLASS						
DIMYAN, MAGID Y 2825			2825	716-004000						
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PLEASE NOTE: Unlescordation as set forting (A) NAME OF ASSICE.		fied b	elow, no assignee of this form is NO						cument has been filed for	
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ADVANCED MICRO DEVICES, INC. Sunnyvale, California										
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